

WHAT IS CLAIMED IS:

1. A tint control system for component video signals comprising:
 - a first input for receiving a first component video signal;
 - 5 a second input for receiving a second component video signal;
 - circuitry including a first differential amplifier and a second differential amplifier for receiving the first and second component video signals from the first and second inputs, respectively;
 - a first output connected to the circuitry for outputting a first tint control adjustment signal
 - 10 for the first component video signal; and
 - a second output connected to the circuitry for outputting a second tint control adjustment signal for the second component video signal.
2. The system according to Claim 1, wherein the first outputted signal is represented as $V+kU-2ckU$, where V represents the first component video signal, U represents the second component video signal, k is a constant, and c is a value greater than or equal to zero and less than or equal to one.
3. The system according to Claim 1, wherein the second outputted signal is represented as $U-kV+2ckV$, where V represents the first component video signal, U represents the second component video signal, k is a constant, and c is a value greater than or equal to zero and less than or equal to one.
4. The system according to Claim 1, wherein the first and second differential
- 25 amplifiers each include a pair of transistors.
5. The system according to Claim 4, wherein a base of a respective transistor of the

pair of transistors of the first differential amplifier is directly connected to a base of a respective transistor of the pair of transistors of the second differential amplifier.

6. The system according to Claim 4, wherein the emitters of each pair of transistors are connected to ground via a transistor connected in series with a resistor.

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7. The system according to Claim 4, wherein a collector of one transistor of each pair of transistors is connected to an operating voltage.

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8. The system according to Claim 4, wherein a collector of one transistor of the pair of transistors of the first differential amplifier is connected to the first input via a resistor and to the first output.

9. The system according to Claim 4, wherein a collector of one transistor of the pair of transistors of the second differential amplifier is connected to the second input via a resistor and to the second output.

10. The system according to Claim 4, wherein a base of one transistor of the pair of transistors of the first differential amplifier is connected to a third input via a resistor for receiving a control signal for the first component video signal.

11. The system according to Claim 4, wherein a base of one transistor of the pair of transistors of the second differential amplifier is connected to a third input via a resistor for receiving a control signal for the first component video signal.

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12. A method for controlling tint of component video signals, the method comprising the steps of:

receiving a first component video signal;

receiving a second component video signal;

providing circuitry including a first differential amplifier and a second differential amplifier for receiving the first and second component video signals, respectively; outputting a first tint control adjustment signal for the first component video signal; and outputting a second tint control adjustment signal for the second component video signal.

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13. The method according to Claim 12, wherein the first outputted signal is represented as $V+kU-2ckU$, where V represents the first component video signal, U represents the second component video signal, k is a constant, and c is a value greater than or equal to zero and less than or equal to one.

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14. The method according to Claim 12, wherein the second outputted signal is represented as $U-kV+2ckV$, where V represents the first component video signal, U represents the second component video signal, k is a constant, and c is a value greater than or equal to zero and less than or equal to one.

15. The method according to Claim 12, wherein the first and second differential amplifiers each include a pair of transistors.

16. The method according to Claim 15, wherein a base of a respective transistor of the pair of transistors of the first differential amplifier is directly connected to a base of a respective transistor of the pair of transistors of the second differential amplifier.

17. The method according to Claim 15, wherein the emitters of each pair of transistors are connected to ground via a transistor connected in series with a resistor.

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18. The method according to Claim 15, wherein a collector of one transistor of each pair of transistors is connected to an operating voltage.

19. The method according to Claim 15, wherein a collector of one transistor of the pair of transistors of the first differential amplifier is connected to an input via a resistor for receiving the first component video signal and to an output for outputting the first signal.

20. The method according to Claim 15, wherein a collector of one transistor of the pair of transistors of the second differential amplifier is connected to an input via a resistor for receiving the second component video signal and to an output for outputting the second signal.

5 21. The method according to Claim 15, wherein a base of one transistor of the pair of transistors of the first differential amplifier is connected to an input via a resistor for receiving a control signal for the first component video signal.

10 22. The method according to Claim 15, wherein a base of one transistor of the pair of transistors of the second differential amplifier is connected to an input via a resistor for receiving a control signal for the first component video signal.